

Interfacing FlashRunner 2.0 with RNSS_IPS devices

Parameter List

DEVICE_SUB_ADDRESS

Sets the upper 4 bits of the I2C slave address of the device. The remaining 3 bits are always "0".

ADR_IRQ_INTERRUPT_ENABLE

Configures the ADR_IRQ pin either as an interrupt pin ("YES") or as an address selector ("NO"). When it is configured as an address selector, the 4-th bit of the slave address is determined by the value of the pin.

ADR_IRQ_SET

Logical value ("0" or "1") of the ADR_IRQ pin.

INTEGRITY_CHECK_ENABLE

Enables CRC and parity bit for transmission error control. Modify this parameter if the device has the configuration bit "prot_integ_check_disable" set.

WARN_ABOUT_SKIPPED_REGISTERS

Many registers of the device are read-only. The driver will not try to write data on those registers, even if the firmware specifies data for them. If "WARN_ABOUT_SKIPPED_REGISTERS" is set to "YES", a warning will be printed on the log whenever a read-only register is skipped during programming.

Command List

The letter after the command names specifies on which memory region the command is to be executed: "F" for the FTP, "I" for the RFU, "R" for the SRB and "S" for the SFR.

Each command supports the parameters <start_addr> and <size>, that specify to execute the command, respectively, from which register and for how many registers.

CONNECT

Powers-on the device and checks the connection.

PROGRAM

PROGRAM <F|R|S>, or **PROGRAM** <F|R|S> <start_addr> <size>

Programs the specified memory region of the device, using the data loaded from the FRB file or the set dynamic memory. Remember that only the data written in the non volatile memory ("F") will remain after a power reset of the device.



VERIFY

VERIFY <F|R> R, or **VERIFY** <F|R> R <start_addr> <size>

Verifies that the data written into the device corresponds to the data of the FRB file / dynamic memory.

READ

READ <F|I|R|S>, or **READ** <F|I|R|S> <start_addr> <size>

Reads the registers of the specified memory of the device, printing it into the communications log of the workbench.

DUMP

DUMP <F|I|R|S>, or **DUMP** <F|I|R|S> <start_addr> <size>

Reads the registers of the specified memory of the device, dumping it into the dumping file.

DISCONNECT

Powers-off the device.